

Fig. 1A
(Prior Art)

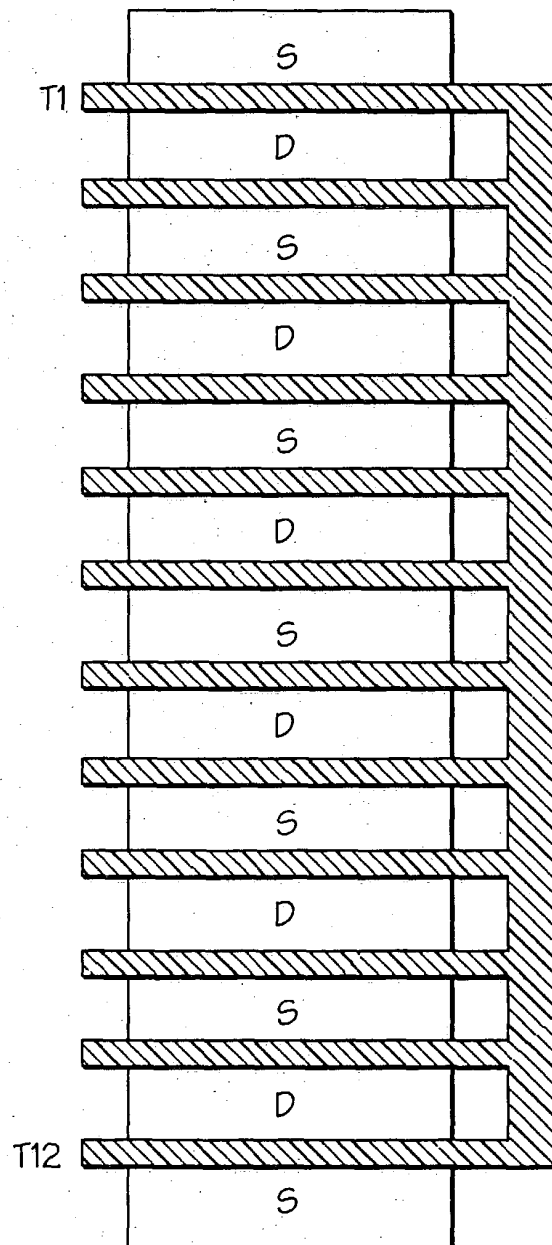


Fig.1B
(Prior Art)

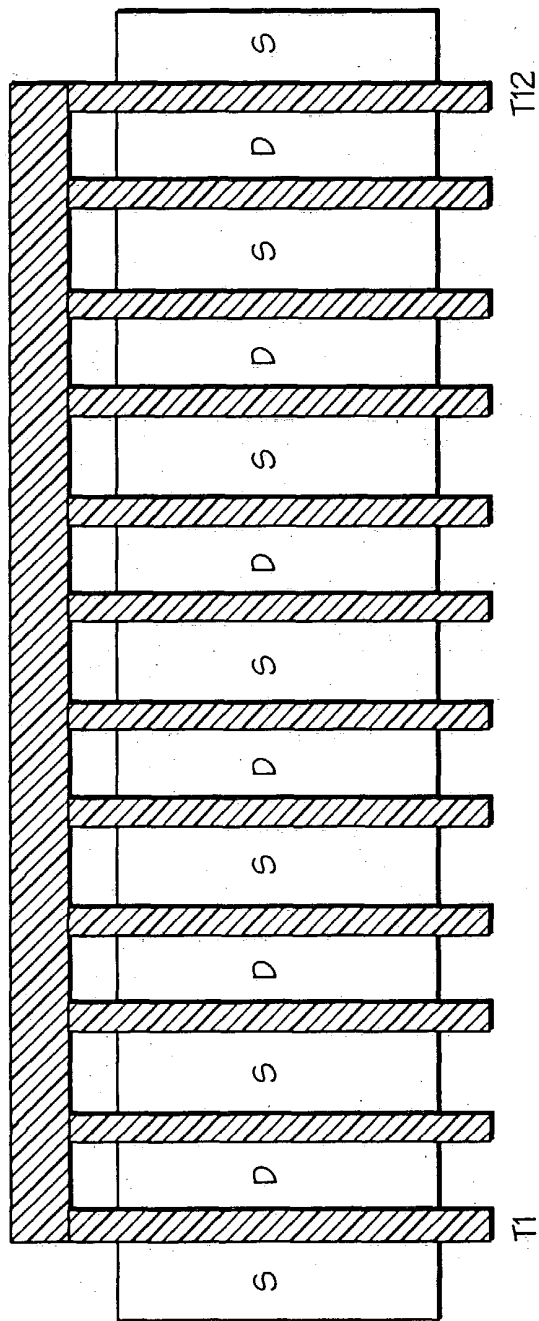


Fig. 1C
(Prior Art)

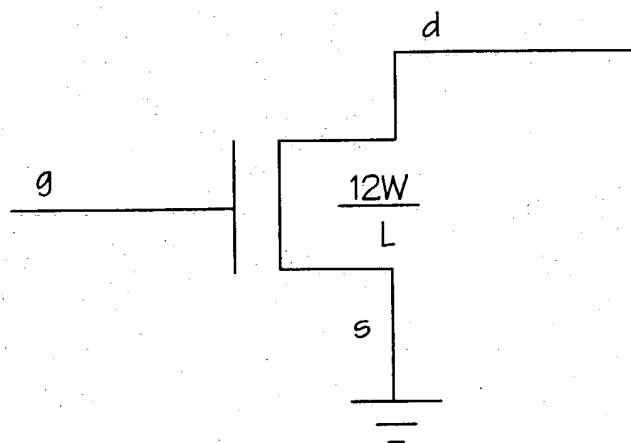


Fig.1D
(Prior Art)

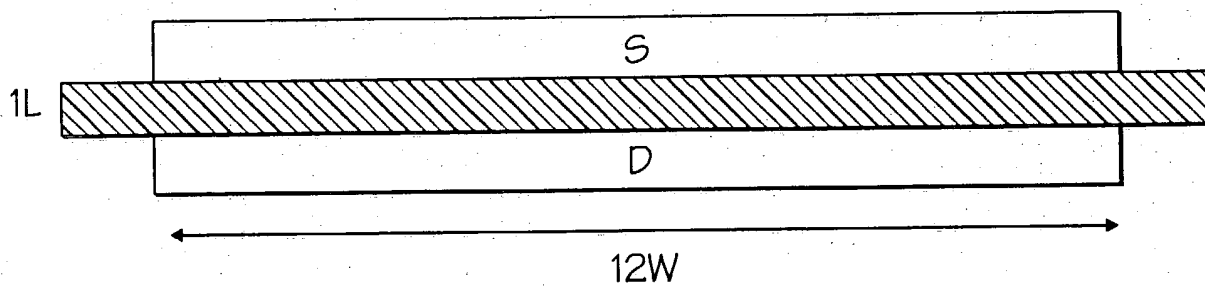
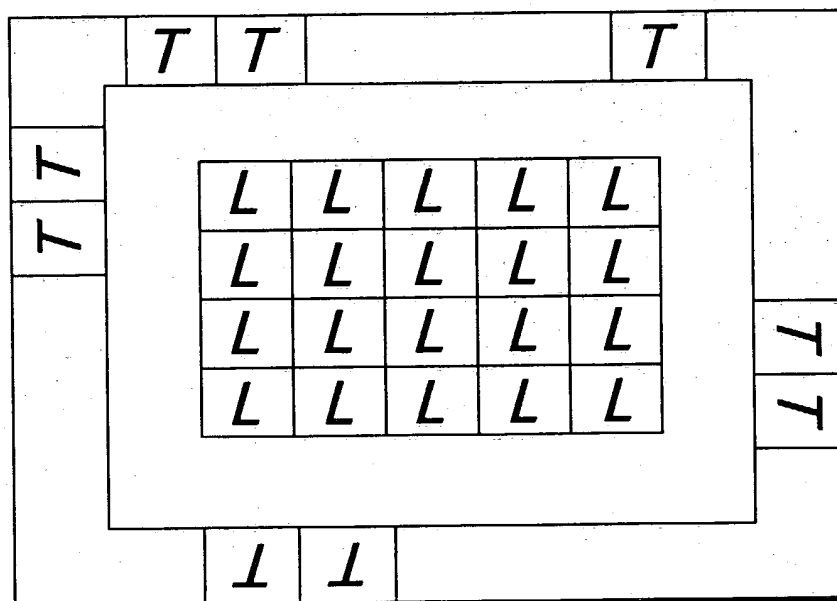


Fig.1E
(Prior Art)



<i>L</i>	Core Cell with <i>L</i> orientation mark
<i>T</i>	I/O Cell with <i>T</i> orientation mark

Fig.2
(PRIOR ART)

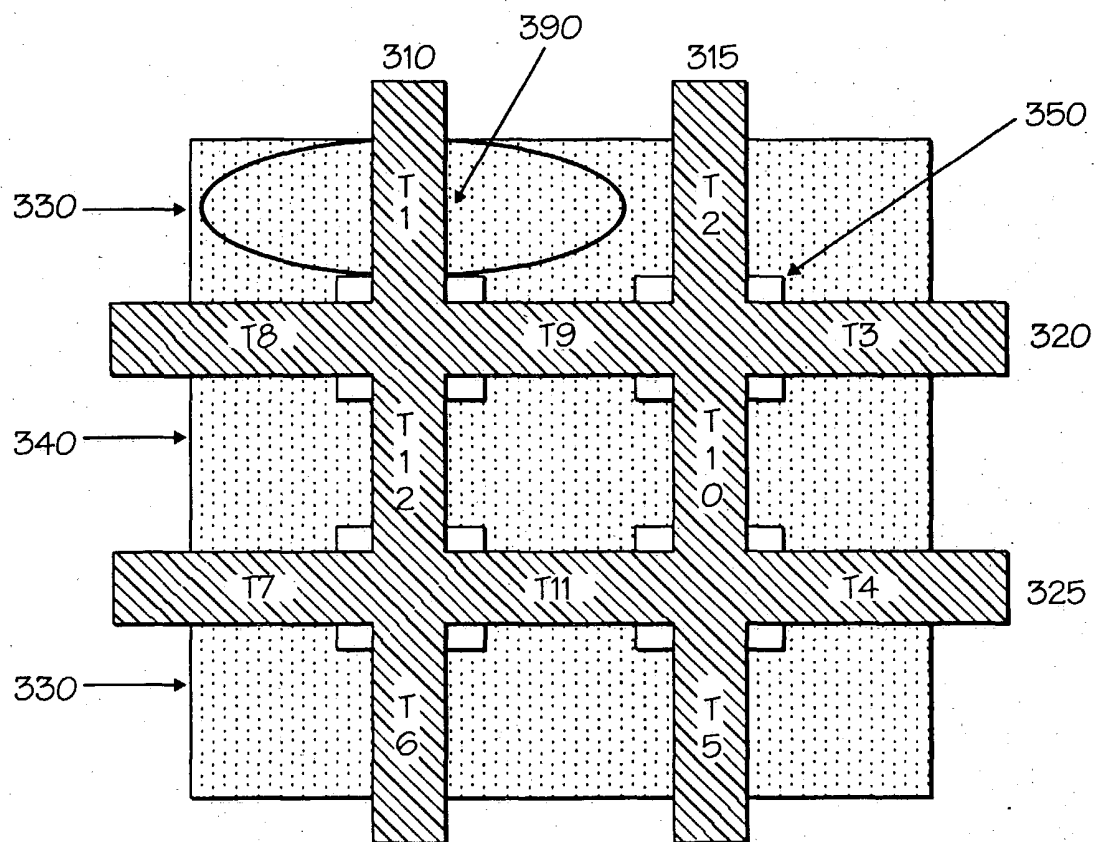


Fig. 3A

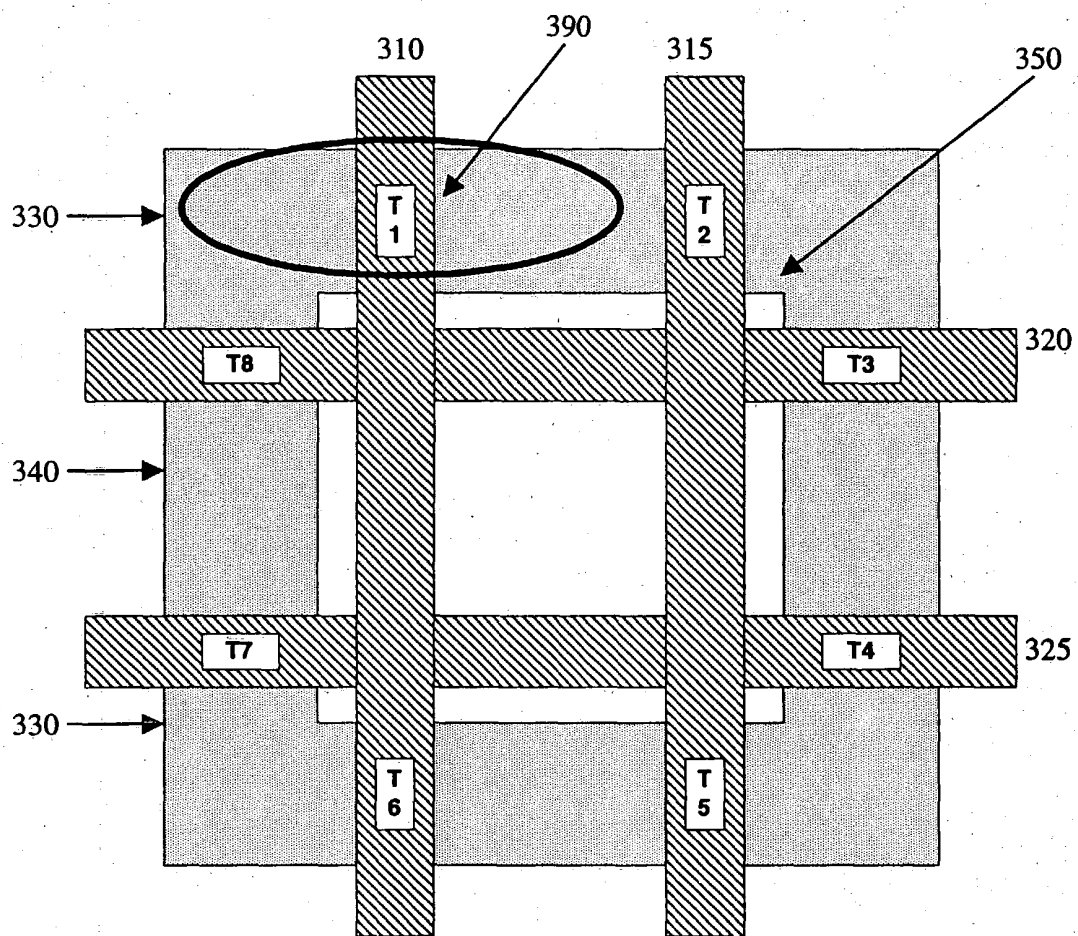


Fig. 3B

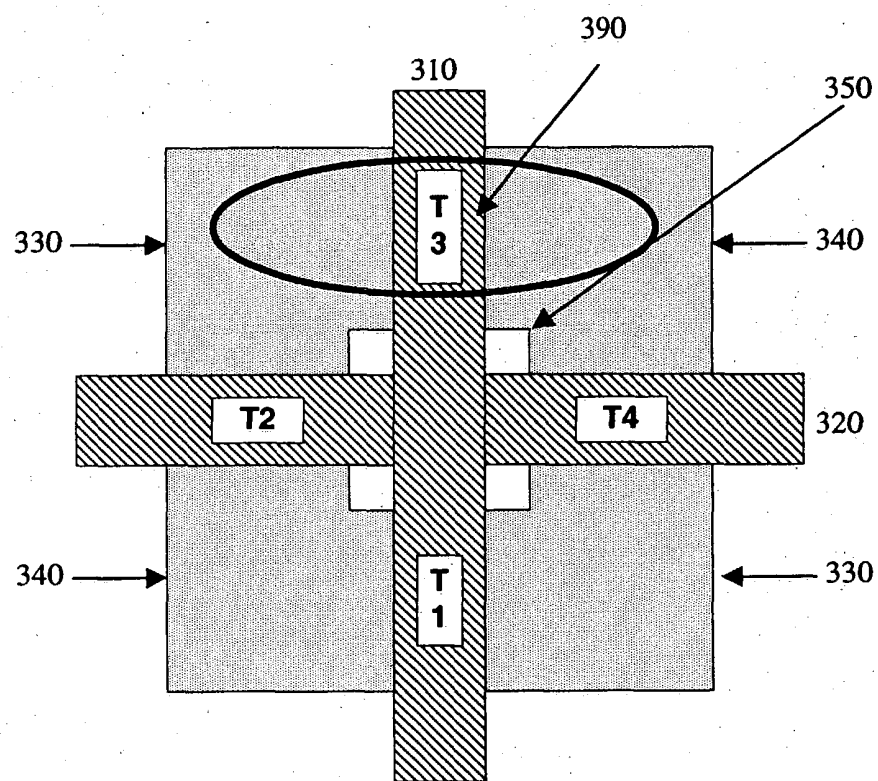


Fig. 3C

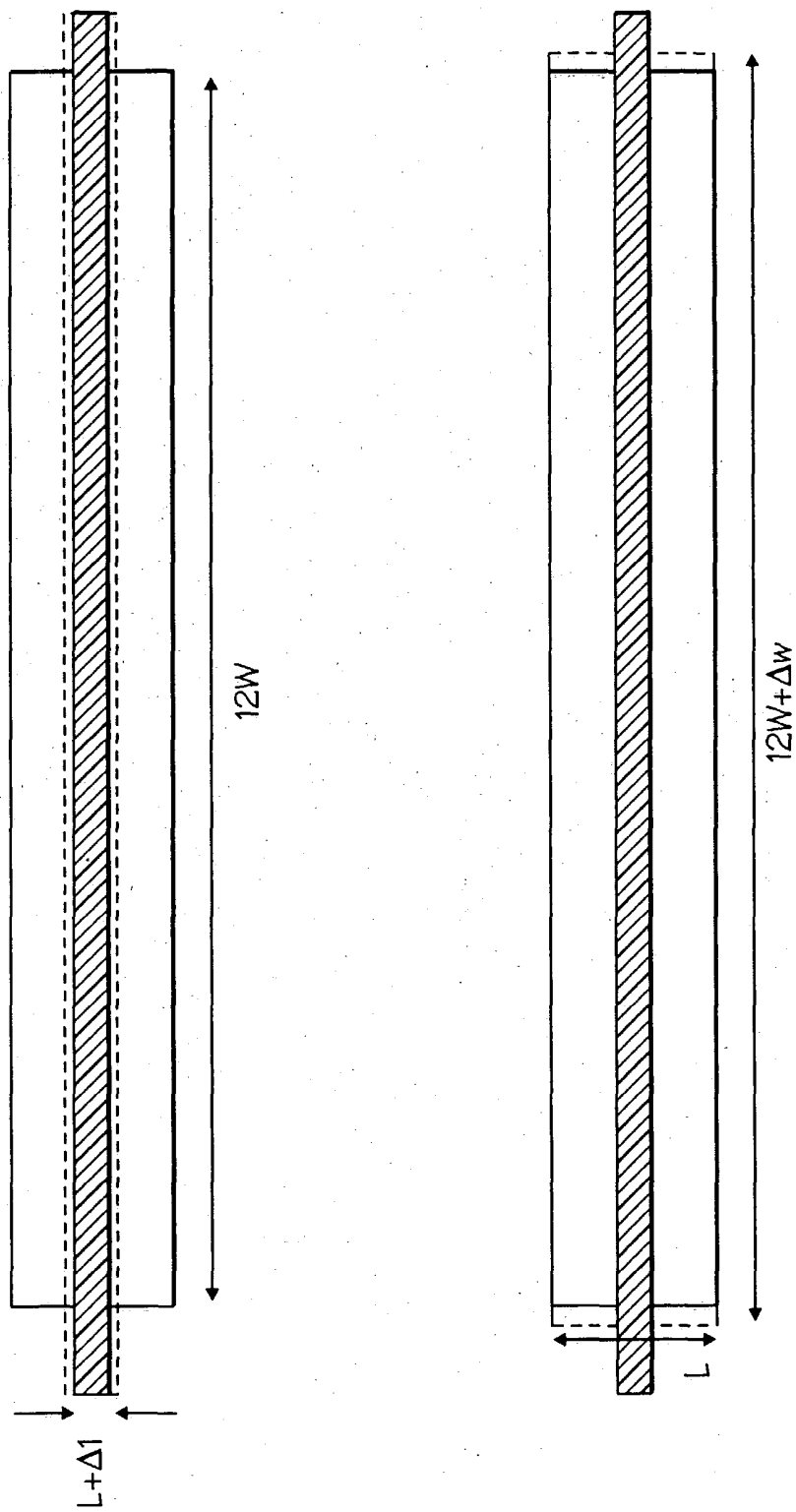
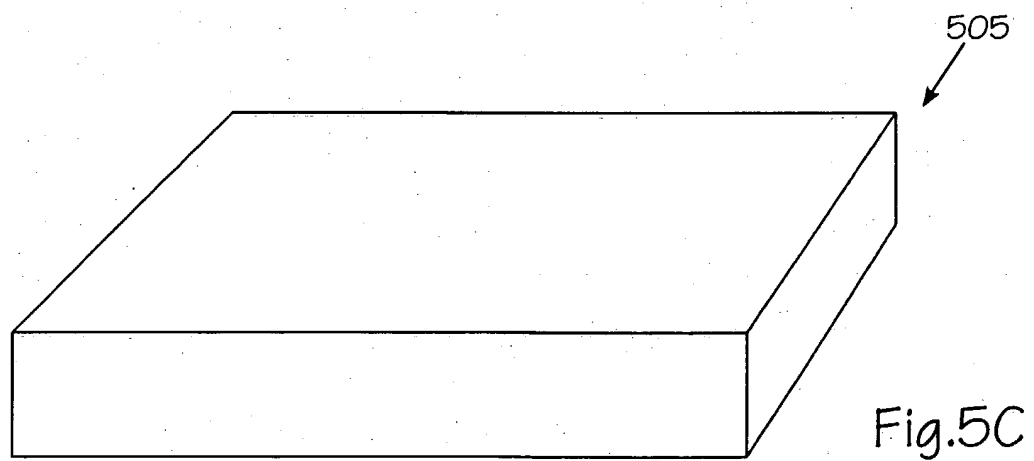
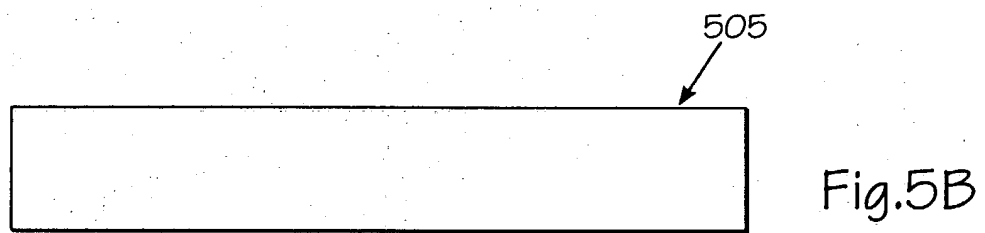
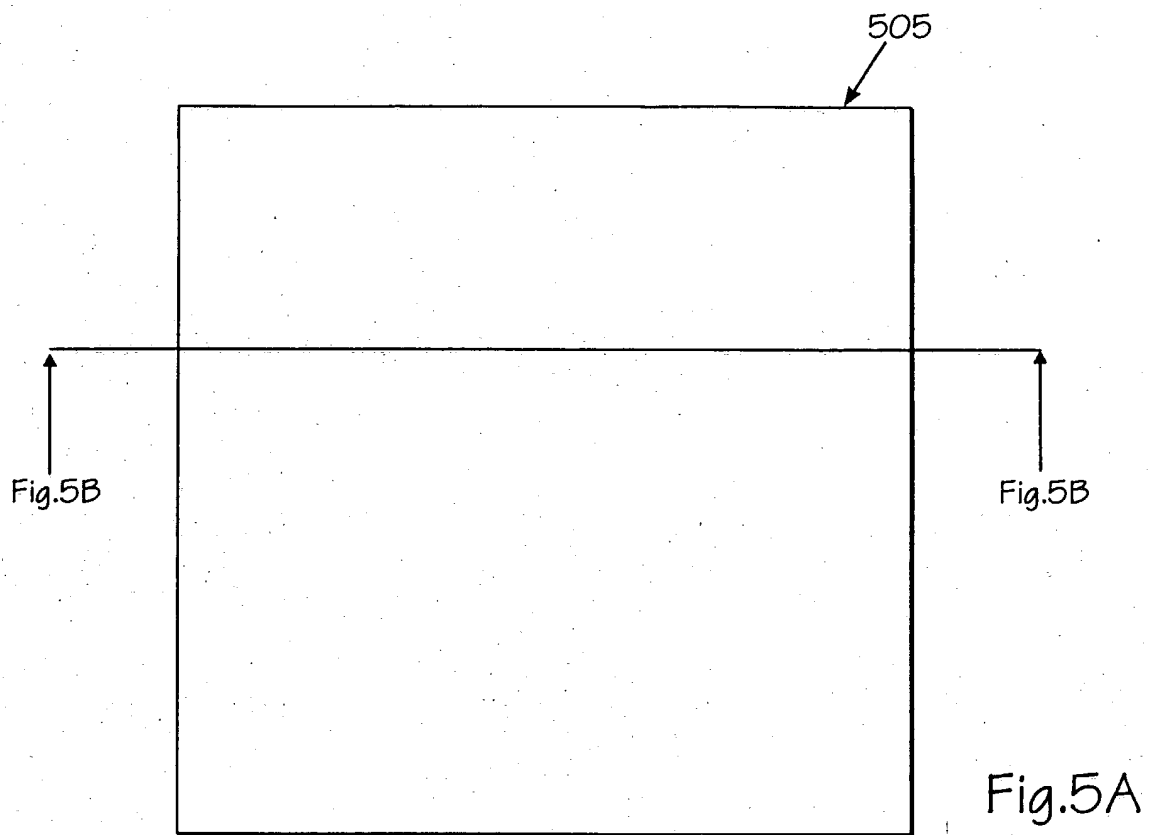
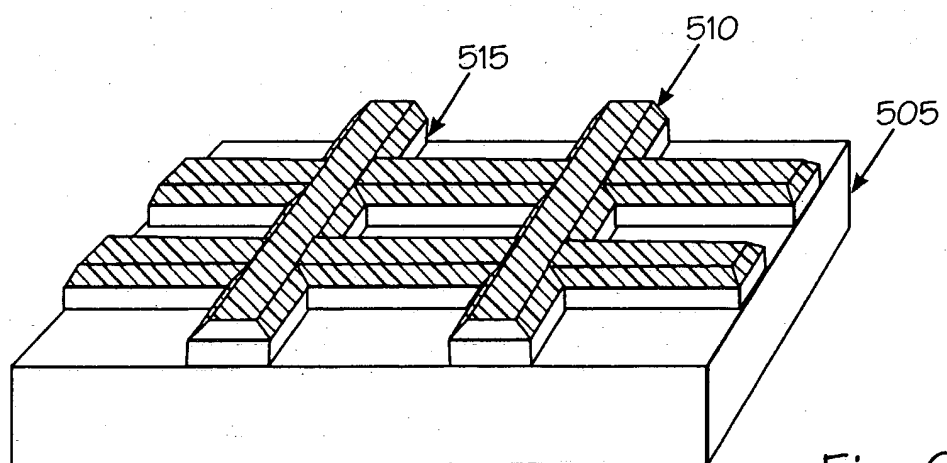
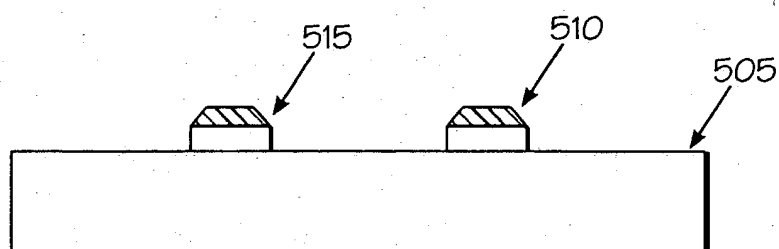
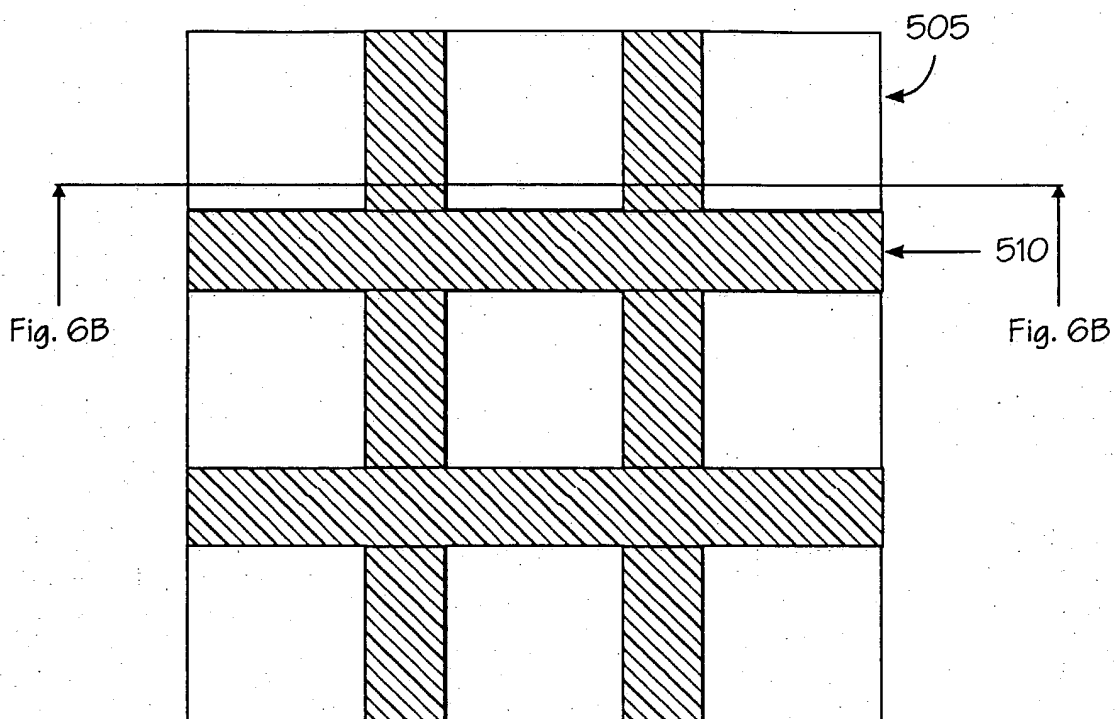


Fig. 4





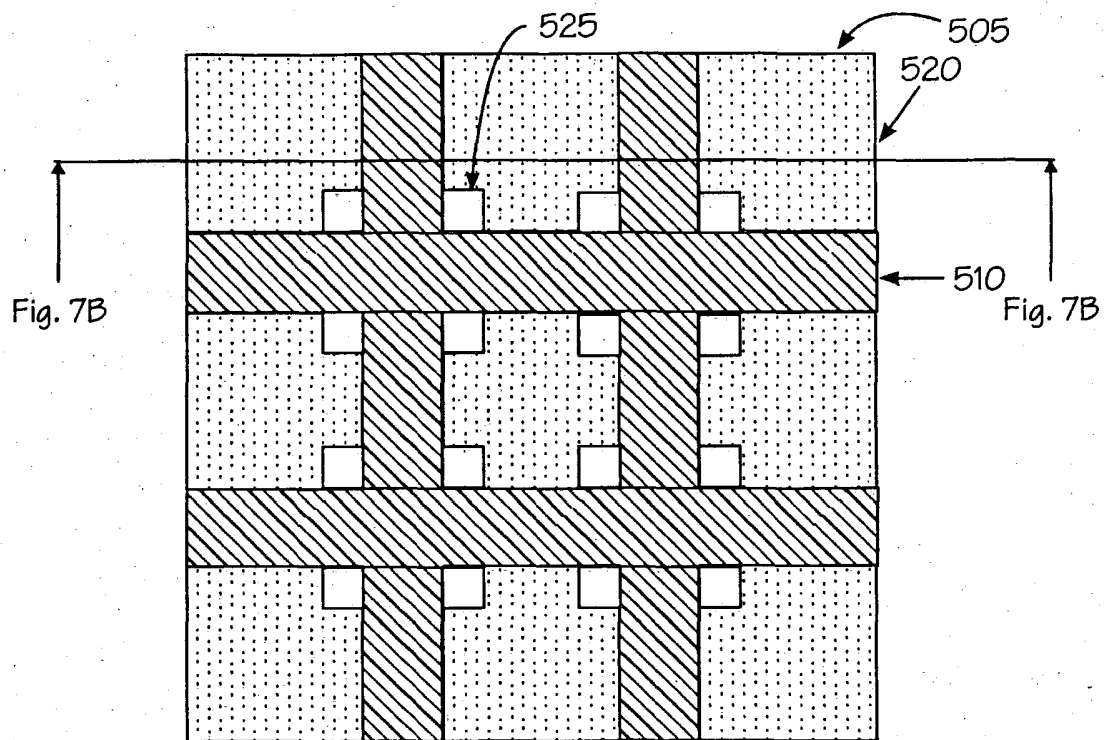


Fig. 7A

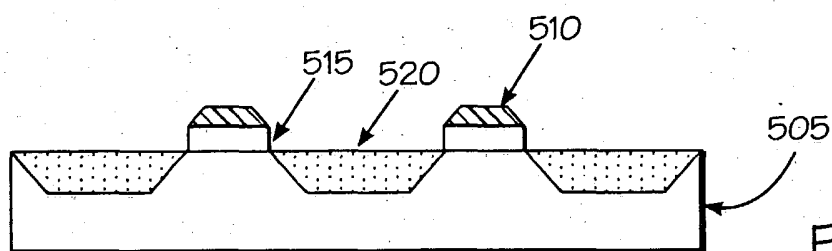


Fig. 7B

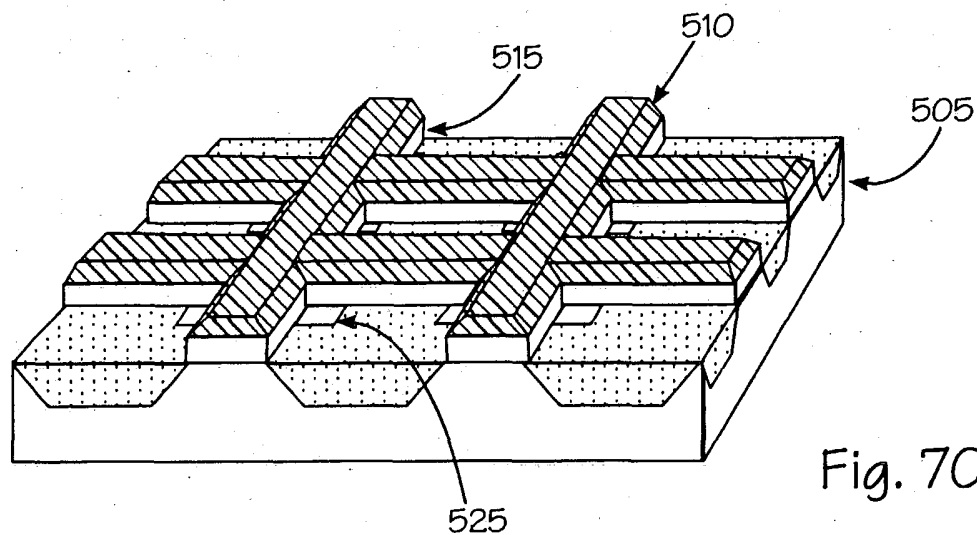
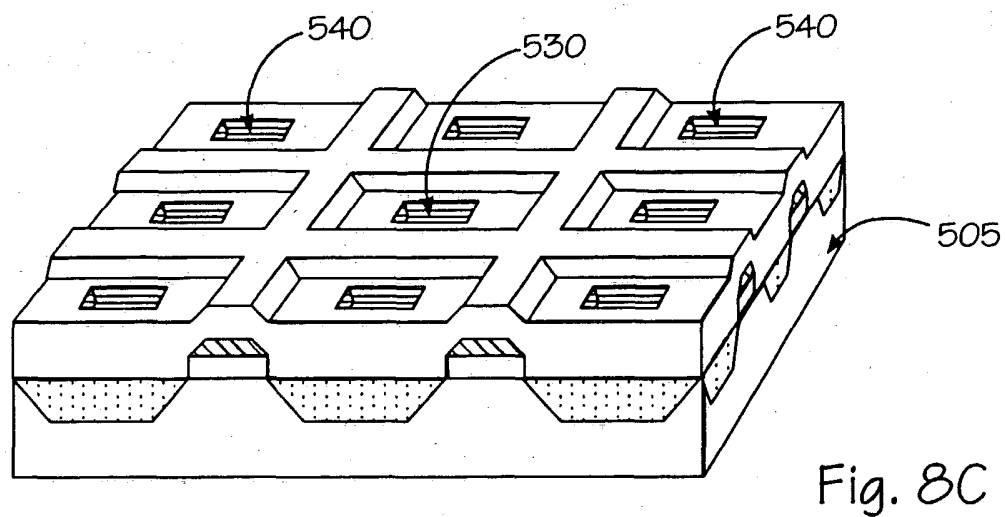
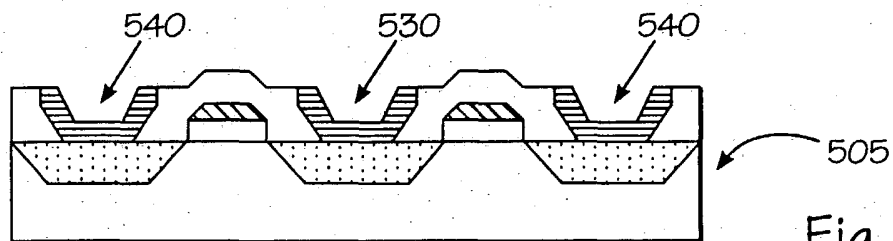
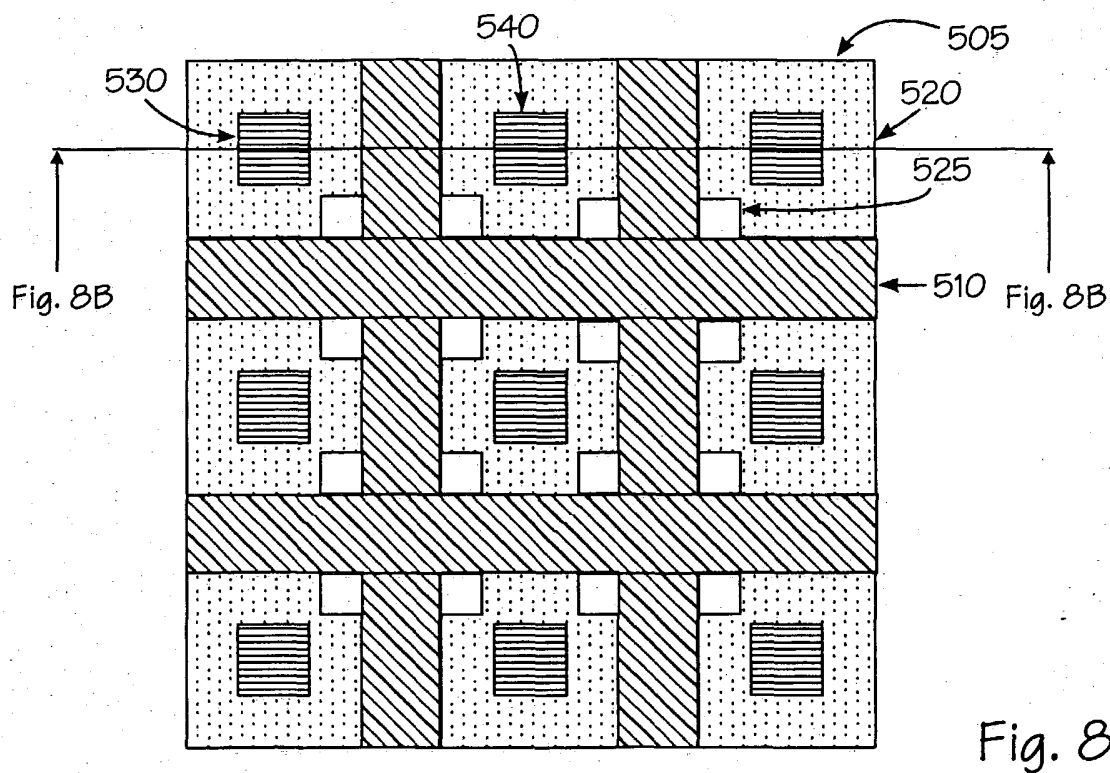


Fig. 7C



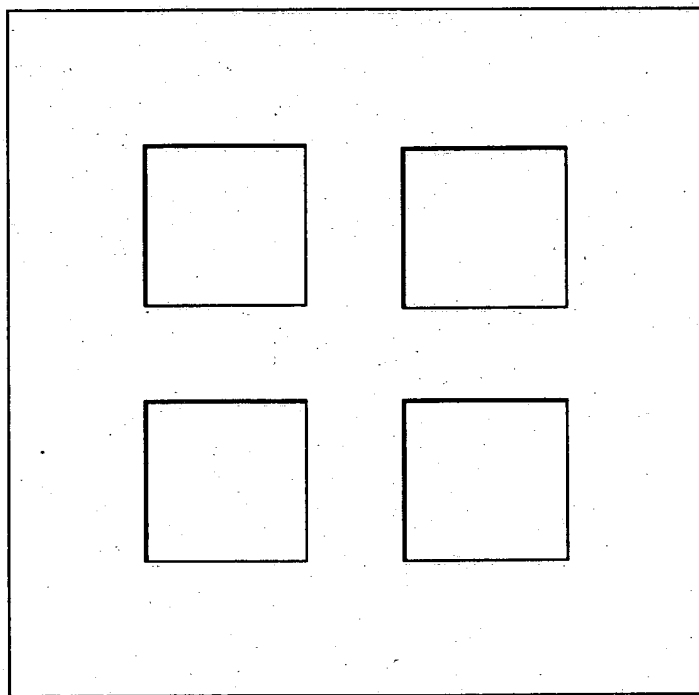


Fig. 9